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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/707,548-Conf. #1545
		Filing Date	December 20, 2003
		First Named Inventor	Munehiro Karasudani
		Art Unit	2811
		Examiner Name	Not Yet Assigned
		Attorney Docket Number	22040-00025-US
Sheet	1	of	1

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

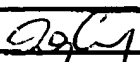
FOREIGN PATENT DOCUMENTS						
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ou	EA	"Multiple Twisted Dateline Techniques for Multigigabit DRAM's", Dong-Sun Min and Dietrich W. Langer, IEEE Journal of Solid-State Circuits, Vol. 34, No. 6, pp. 856-865, June 1999.	

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Examiner Signature		Date Considered	04-14-06
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